

Harish Kumar G

Engineer

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Professional Summary:

As an electrical engineer with a good experience in digital and analogue circuits. I possess a fervent passion for the VLSI industry. I have a proven track record of working in team in the development and integration of designs that seamlessly operate with systems.

Experience

Project

VGA controller – basic test pattern generator in 8bit RGB FPGA implementation

Bit slice ALU– That can perform add sub and or

Async FIFO– parametrised asyc FIFO with variable depth

Education

Post Graduation Diploma in VLSI

CDAC, ACTS Pune.

Bachelors of Engineering - Electrical and Electronics Engineering, 7.44 CGPA

Anna University, Chennai, Tamil Nadu

GATE EE 2020 score - 353

Technical skills

Programming languages System Verilog 1800-2012 , C, C++

Scripting languages Python, bash shell, TCL

RTL design verification methodology UVM

Protocols AHB ABP I2C SPI UART

Synth tools Vivado, Quartus, Yosys

Simulation tools Questa sim, Visualizer, Verilator

Extra-curricular

Social work - at Ayyanar Kovil, Medical camp volunteer

Hobbies

Calligraphy, Gardening, Walking.

Languages Known Tamil, English